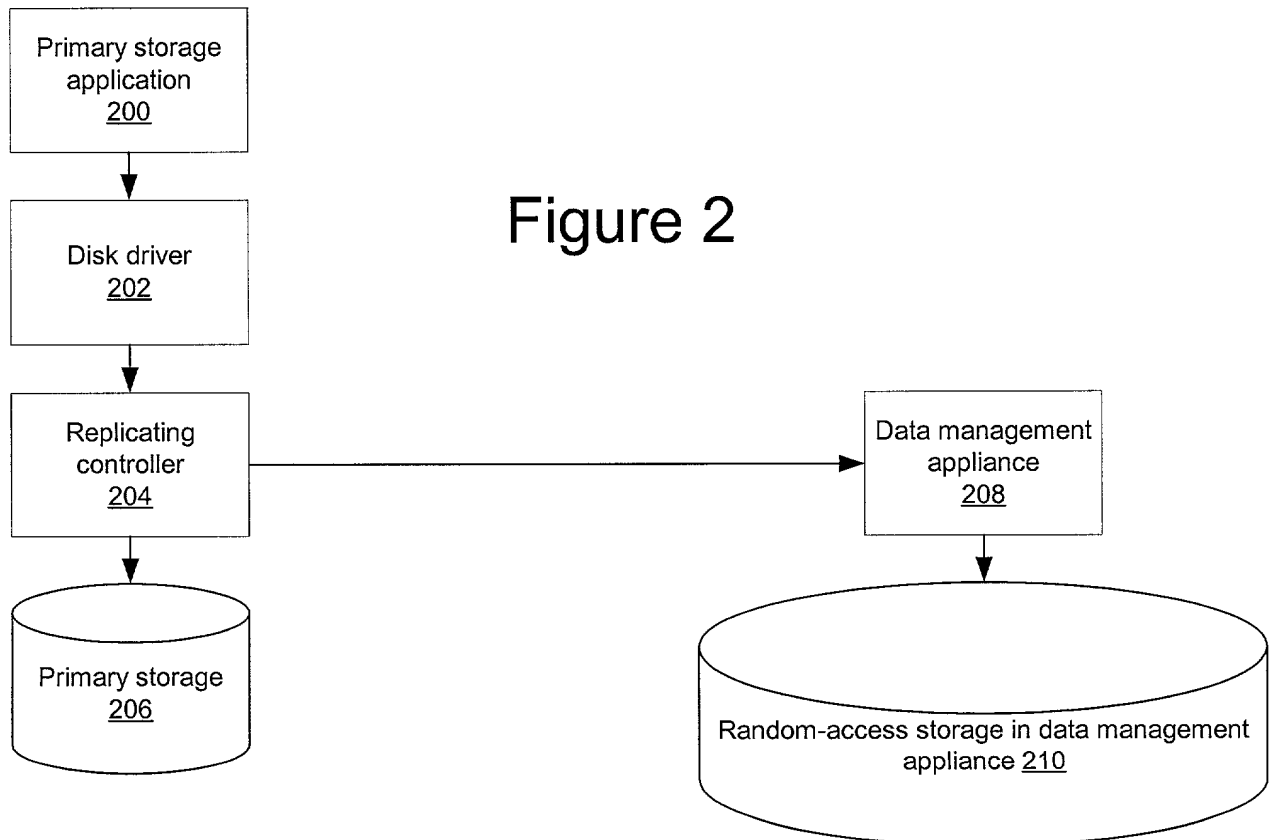
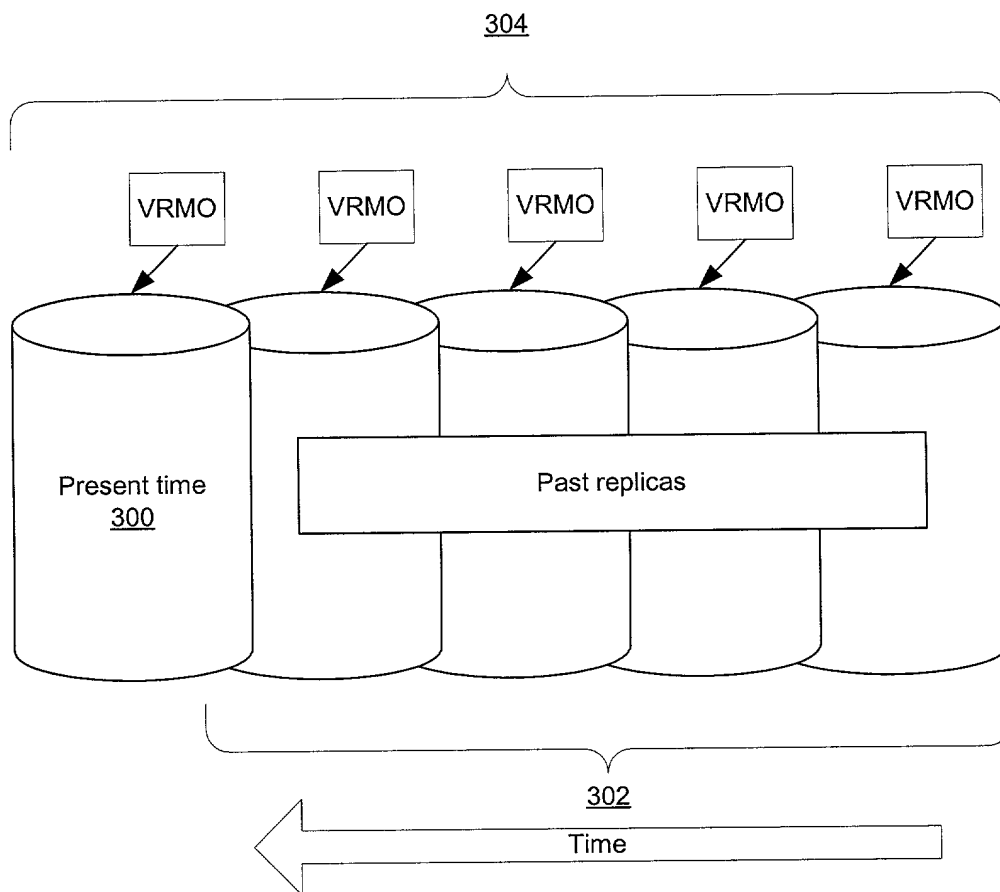


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**Figure 2**



**Figure 3**

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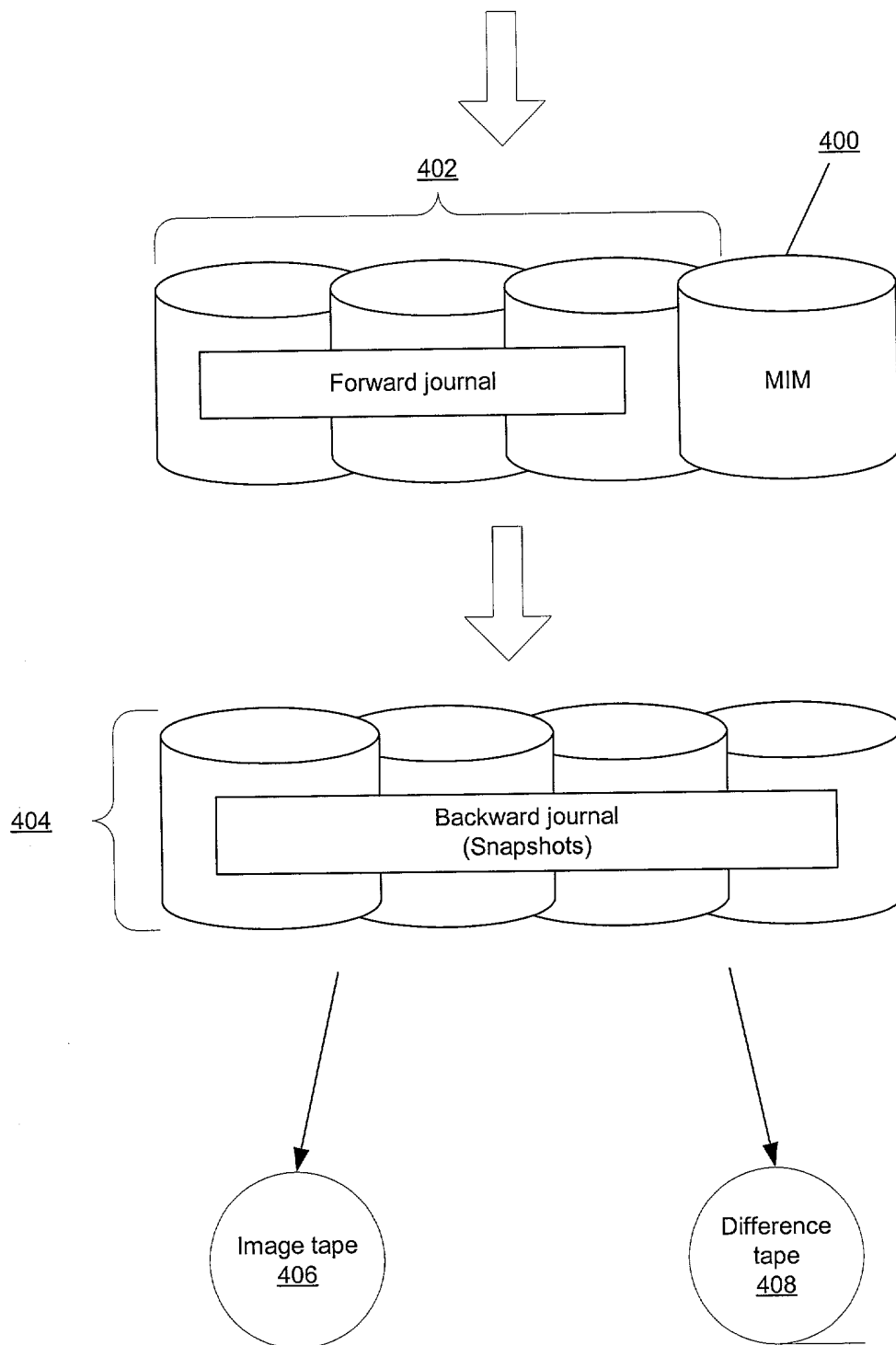
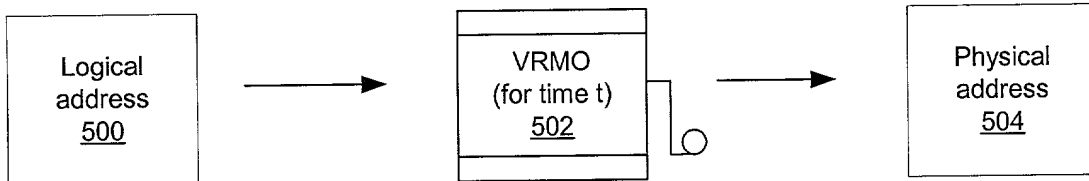
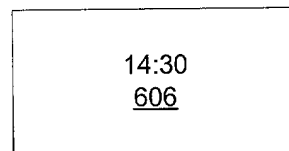
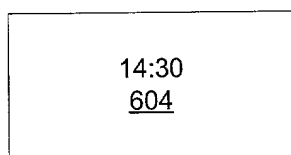


Figure 4

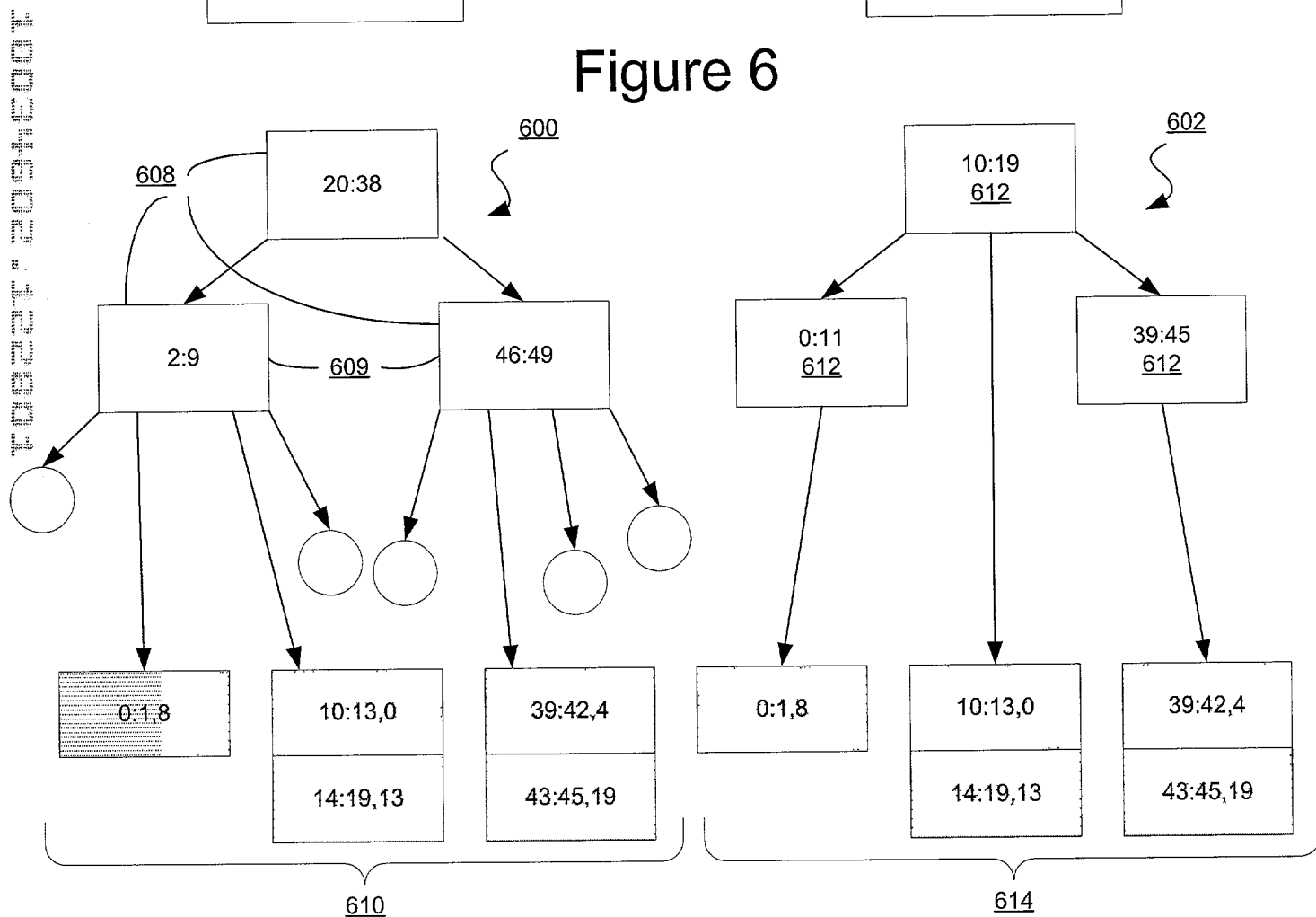


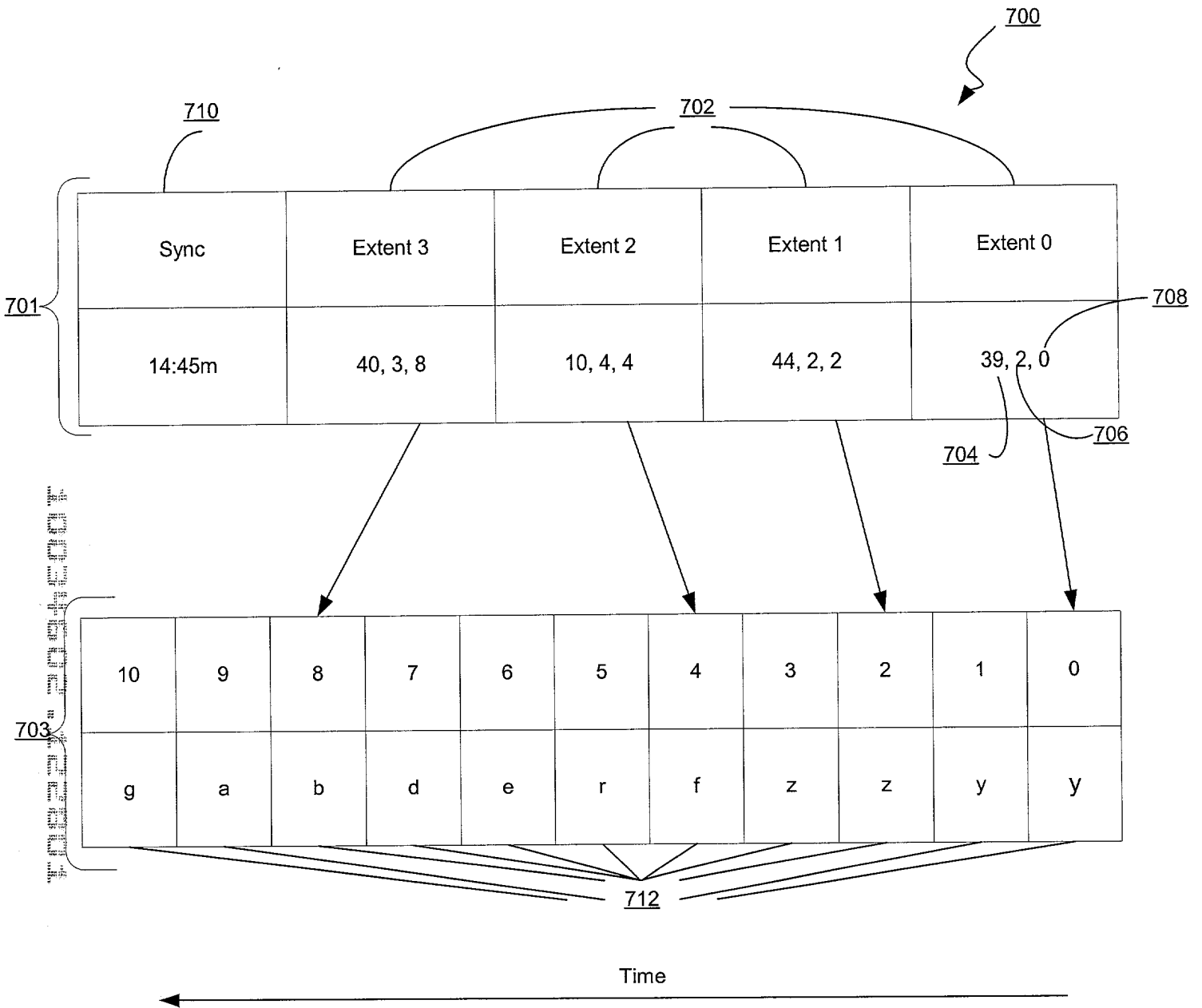
# Figure 5

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# Figure 6





# Figure 7

FIG. 8 is a diagram of a data management appliance 800, which includes a processor 802 and a memory 804.

800

802

804

0	1	2	3	4	5	6	7	8	9
Q	Q	Q	Q	Q	Q	Q	Q	Q	Q
10	11	12	13	14	15	16	17	18	19
R	R	R	R	R	R	R	R	R	R
20	21	22	23	24	25	26	27	28	29
S	S	S	S	S	S	S	S	S	S
30	31	32	33	34	35	36	37	38	39
T	T	T	T	T	T	T	T	T	T
40	41	42	43	44	45	46	47	48	49
U	U	U	U	U	U	U	U	U	U

**Figure 8**  
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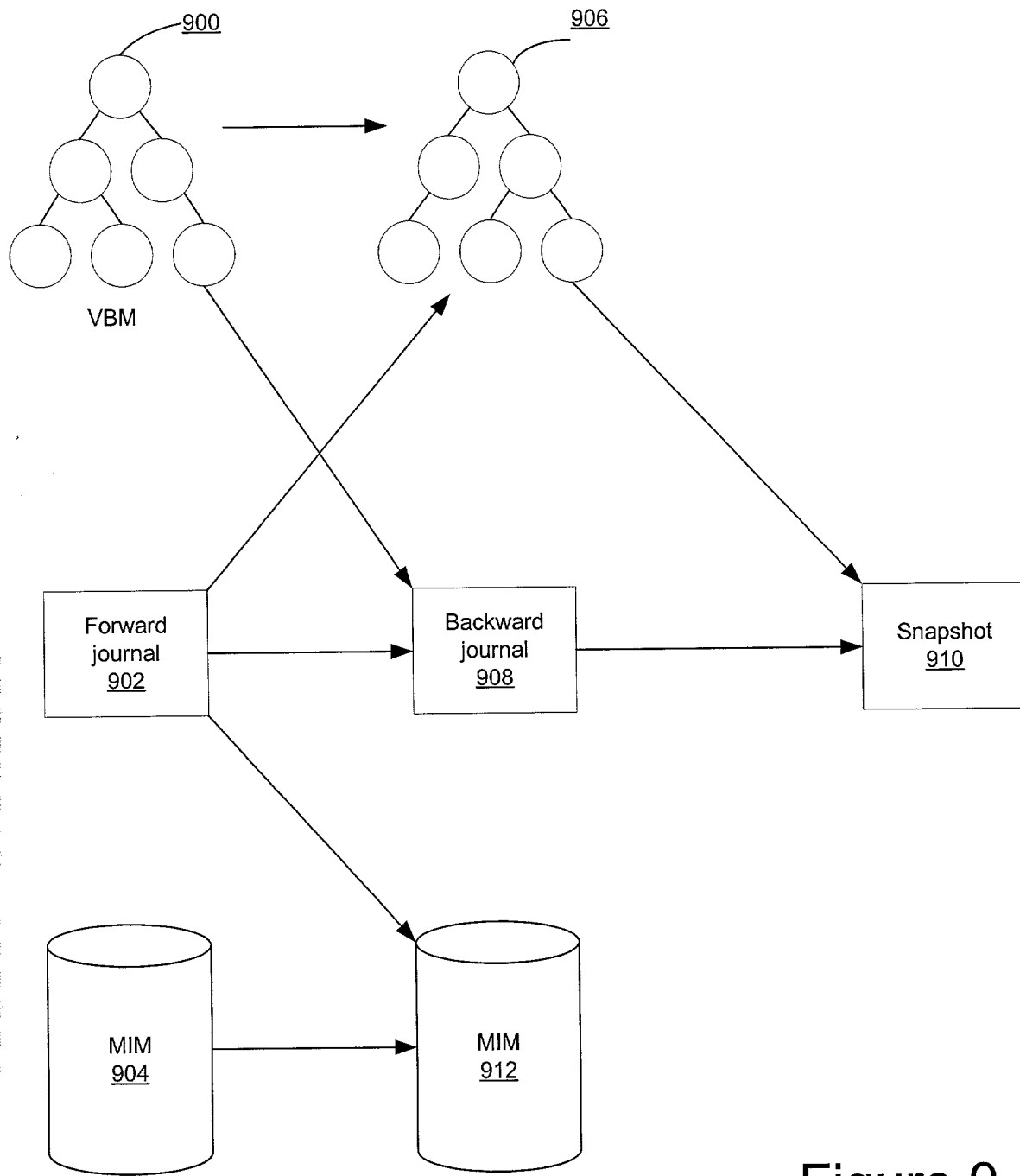


Figure 9

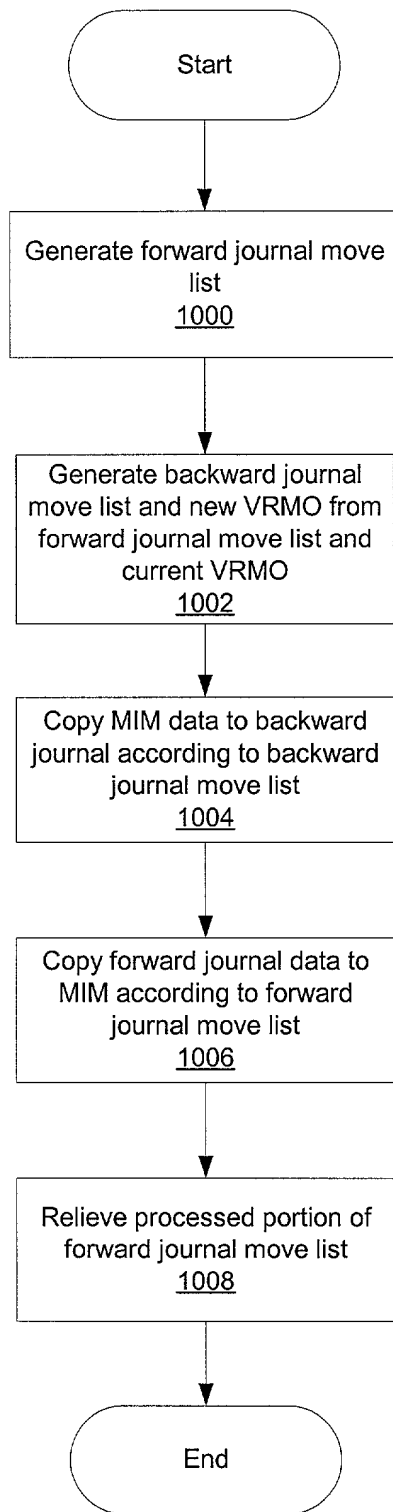
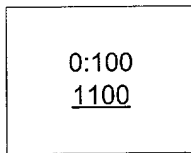


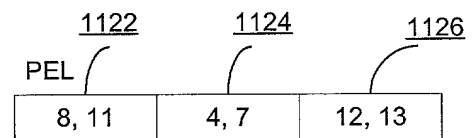
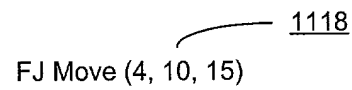
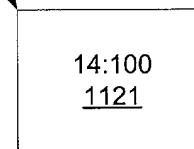
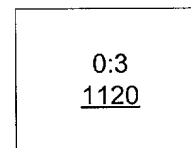
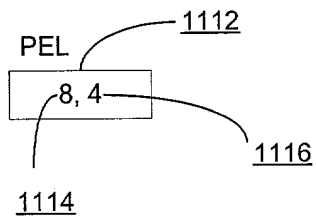
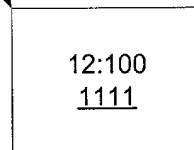
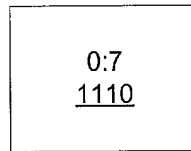
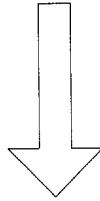
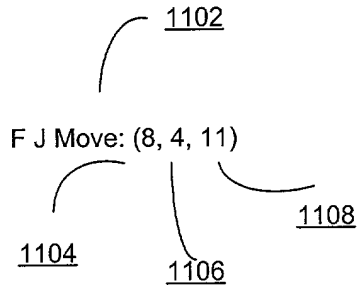
Figure 10





# Figure 11

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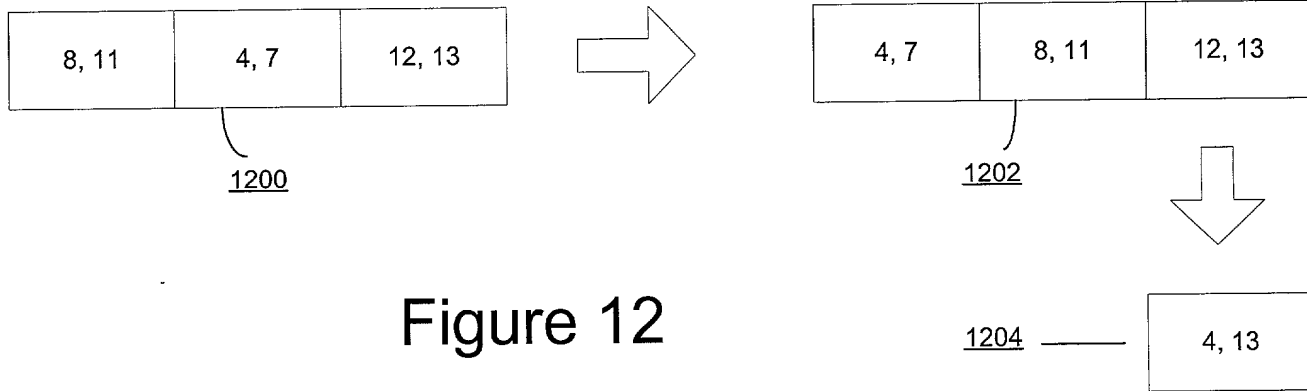


Figure 12

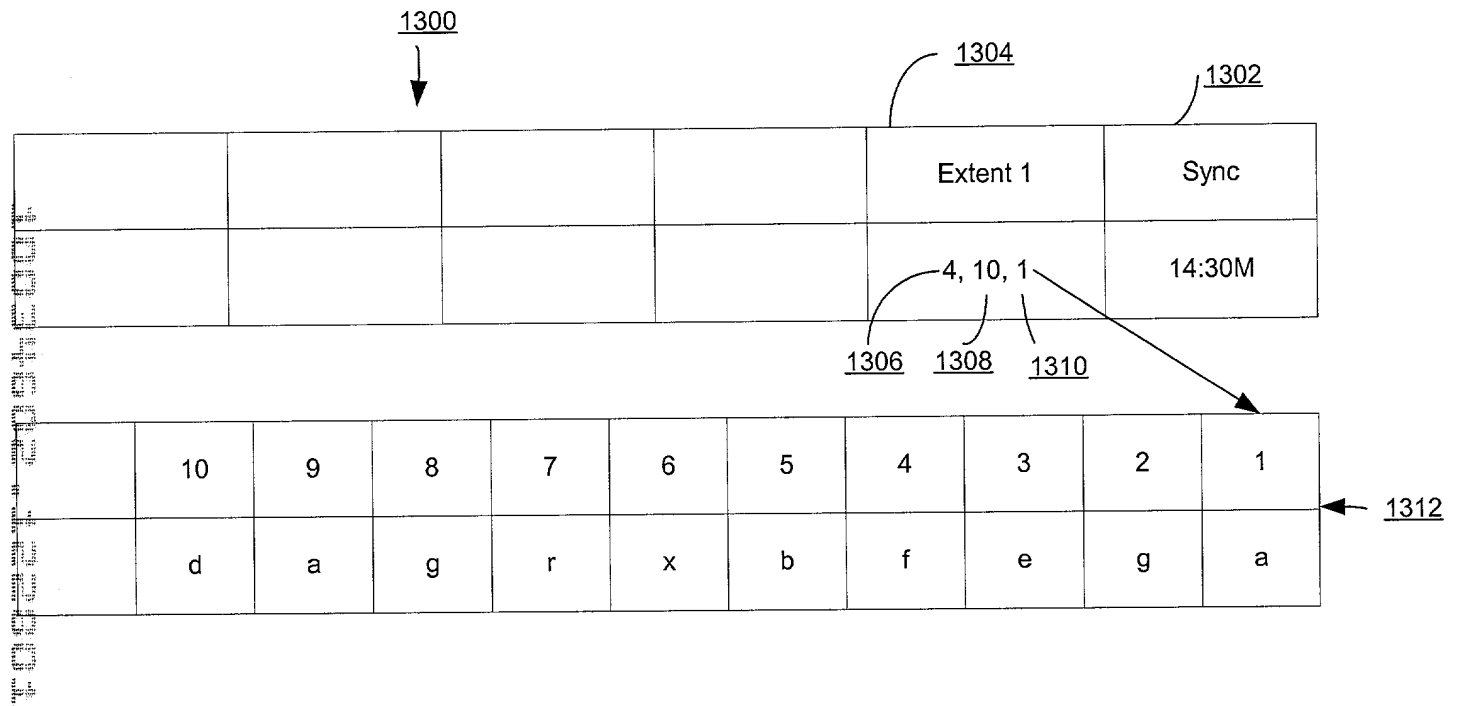


Figure 13

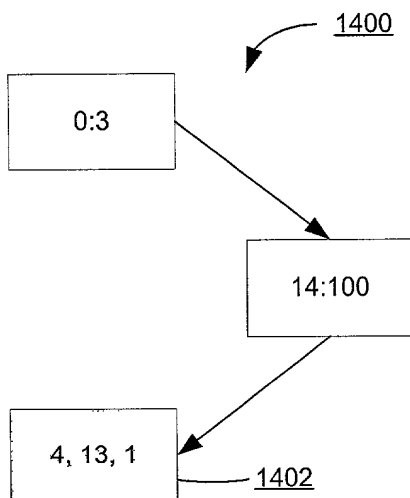


Figure 14

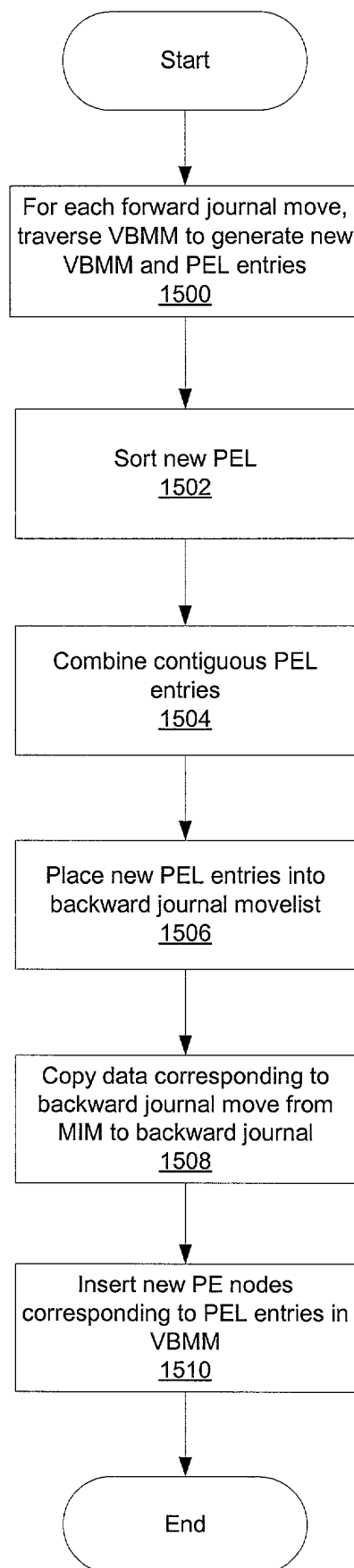
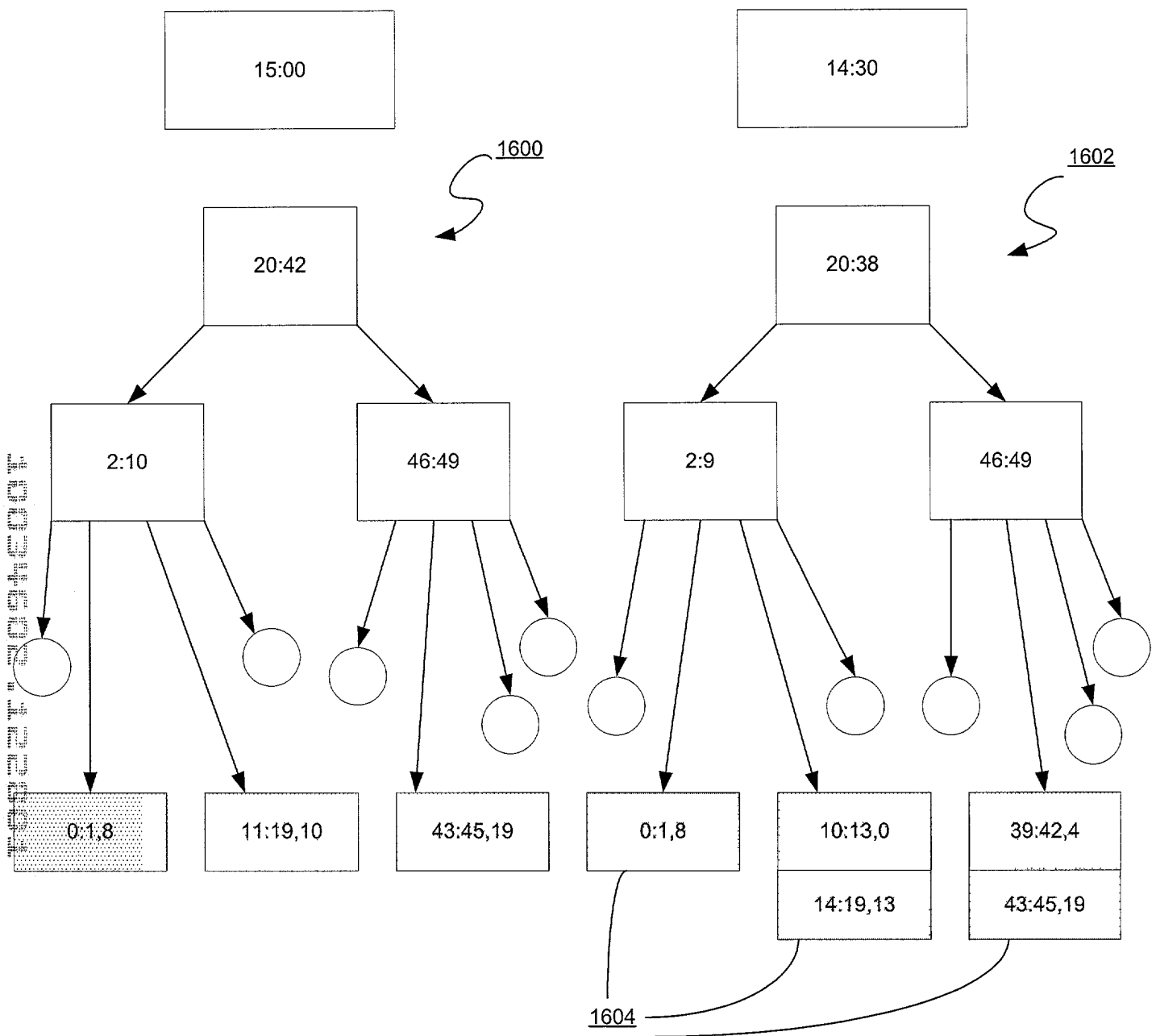


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**Figure 16**

1710

FJ Move (42, 7, 92)

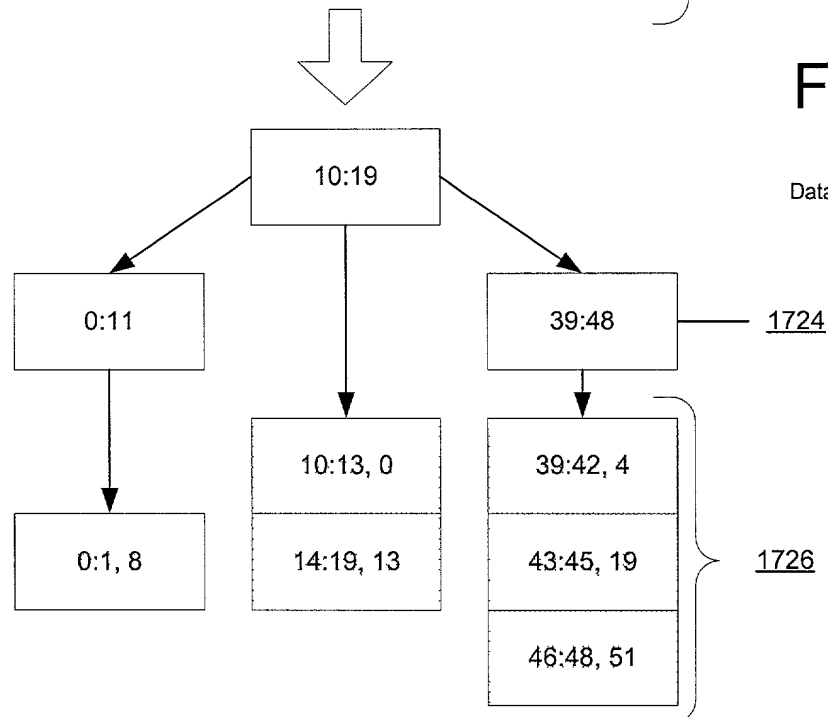
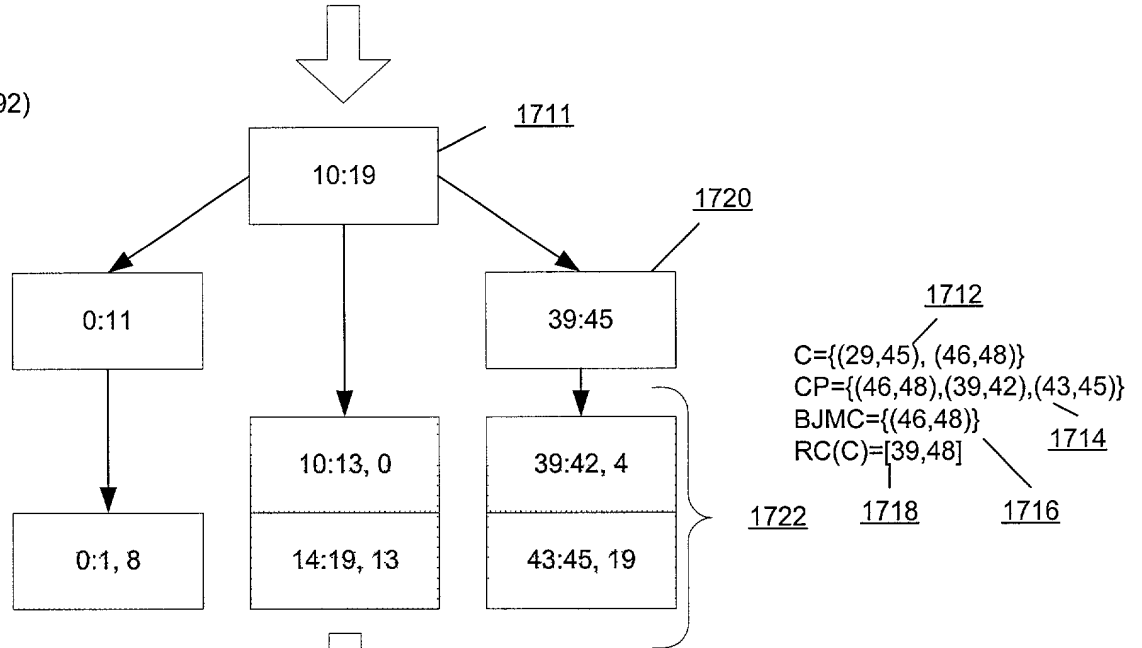
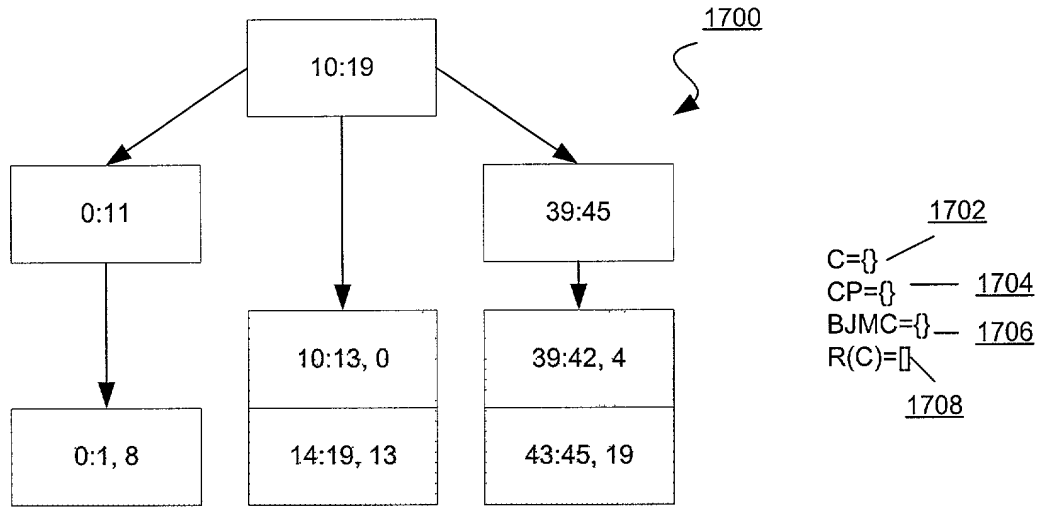


Figure 17

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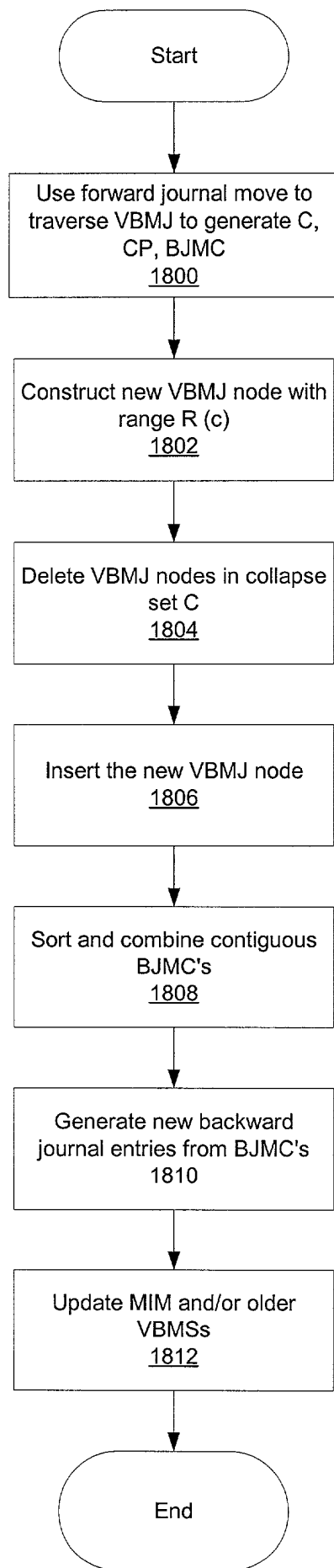


Figure 18

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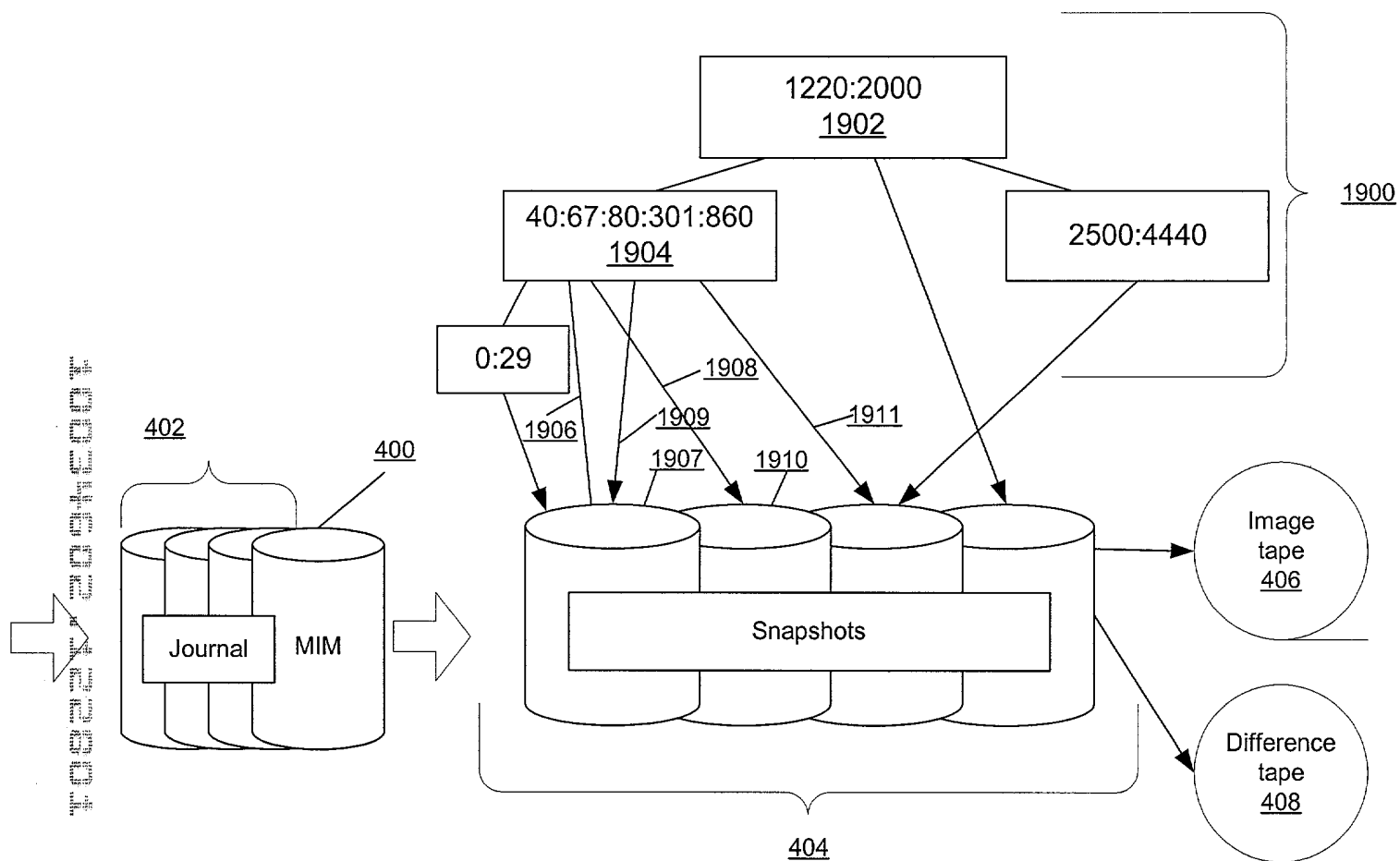


Figure 19

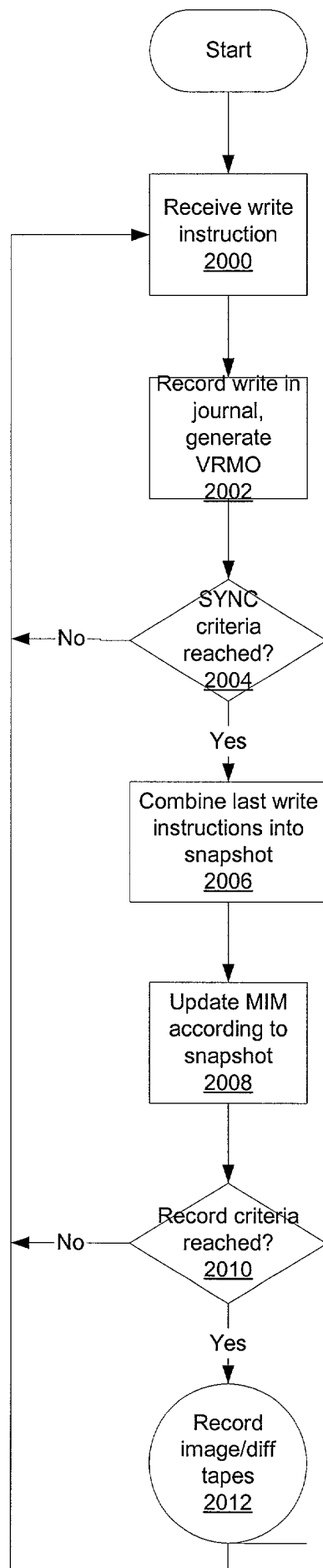


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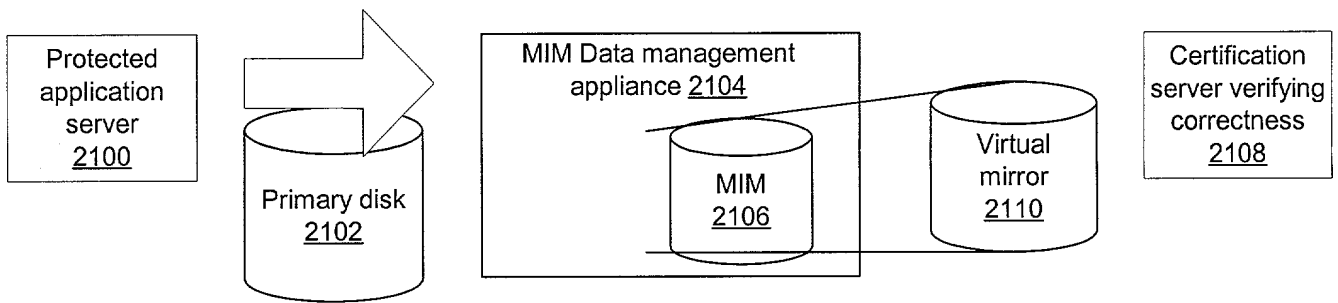


Figure 21

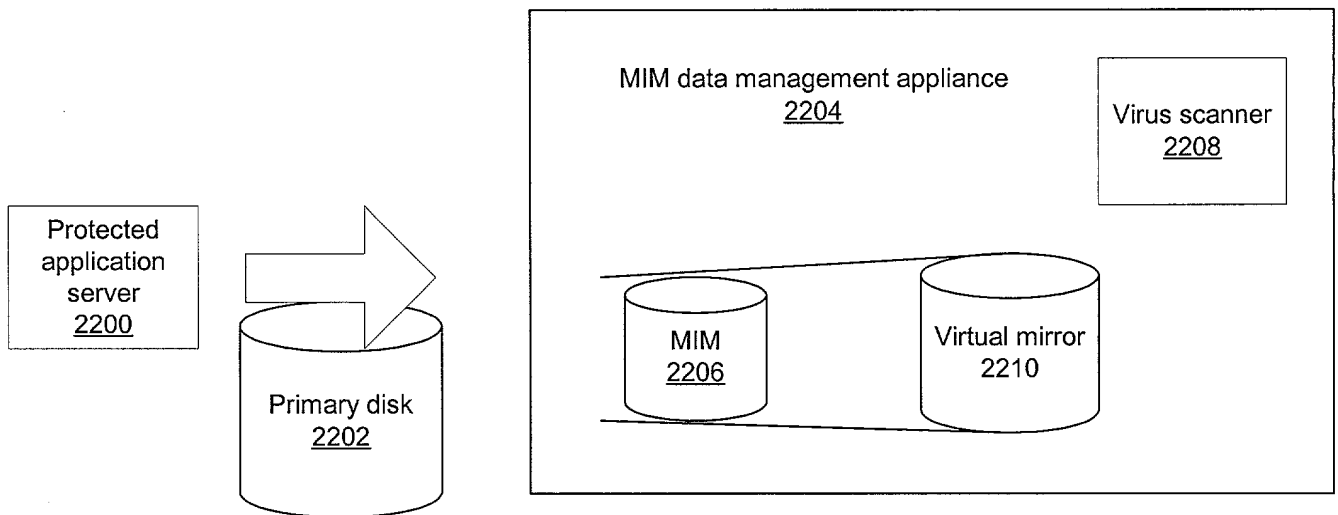


Figure 22

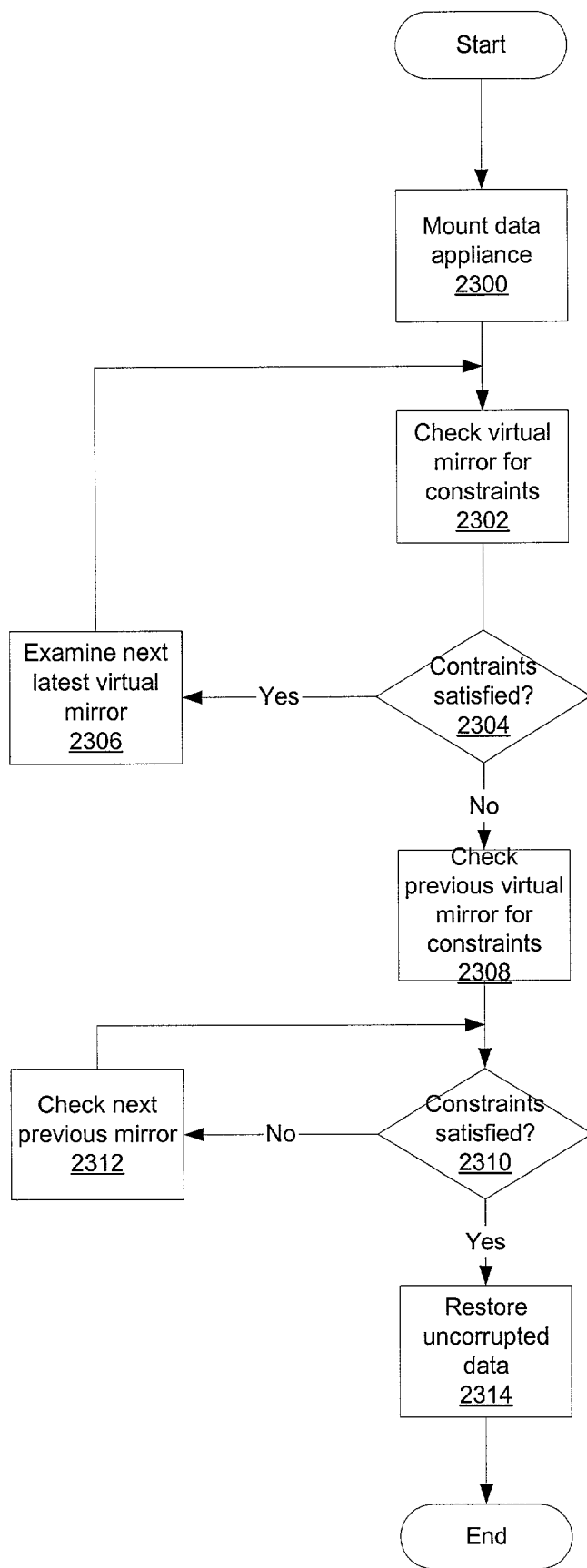
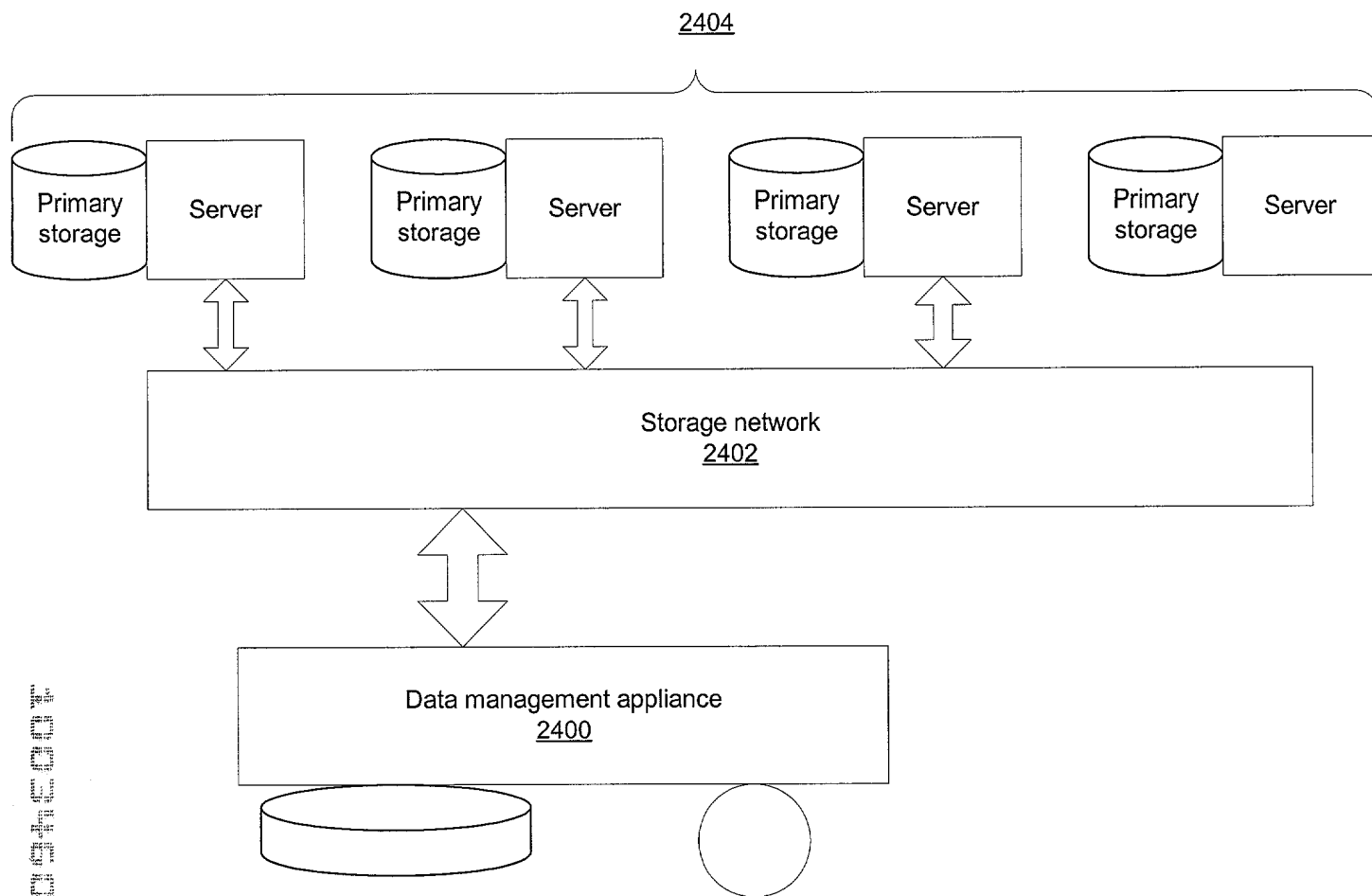


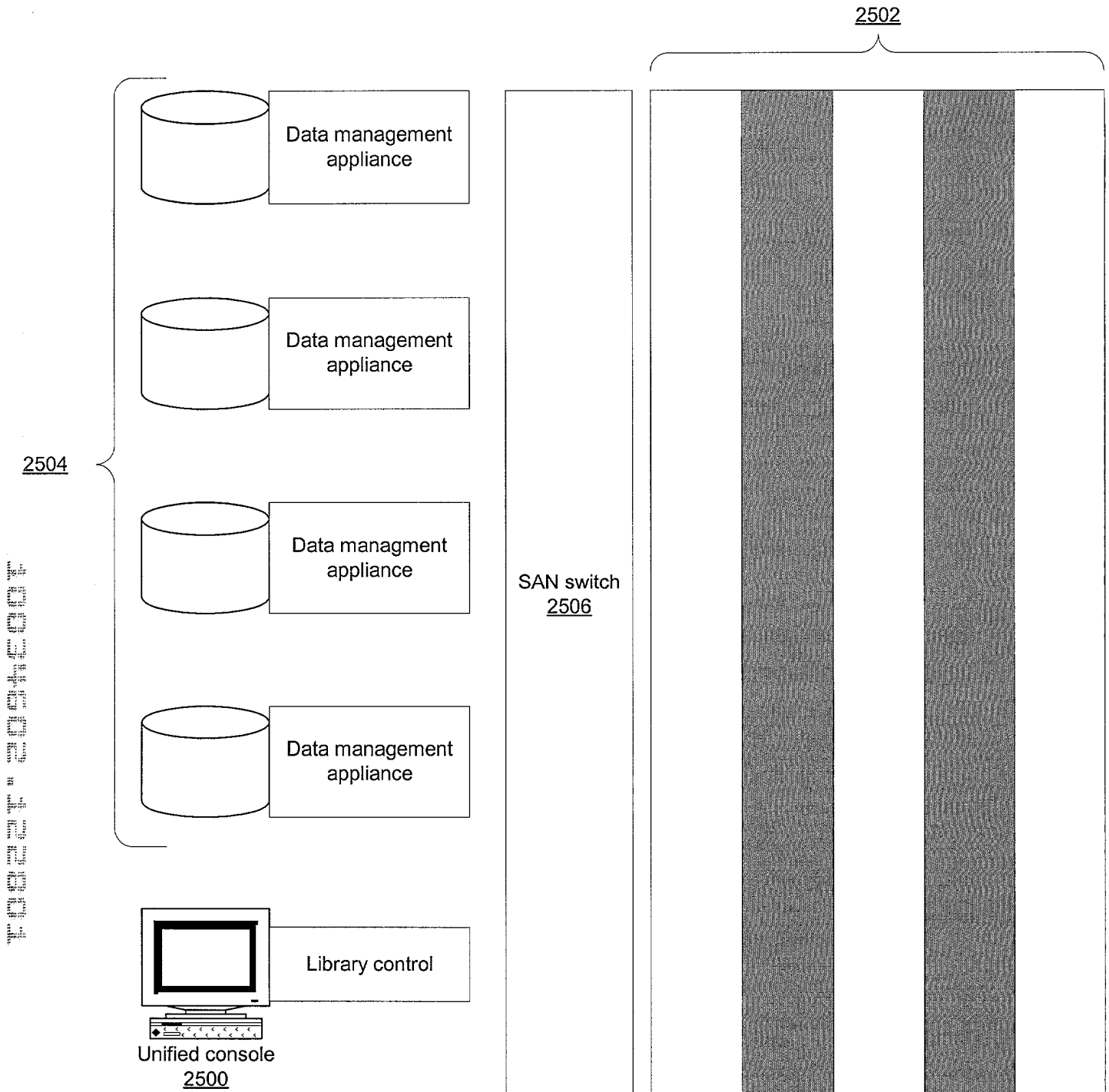
Figure 23

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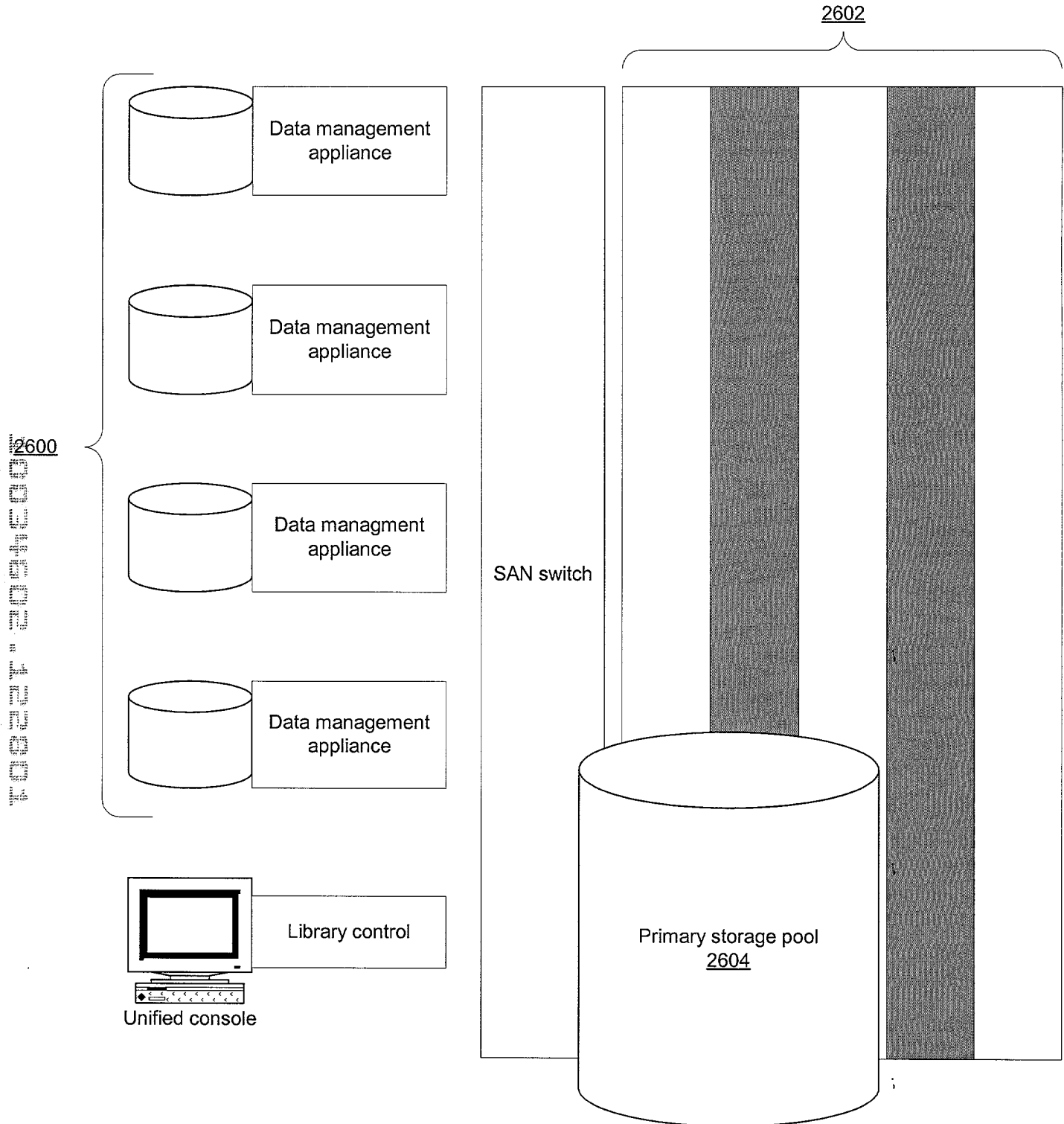


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**Figure 25**



**Figure 26**